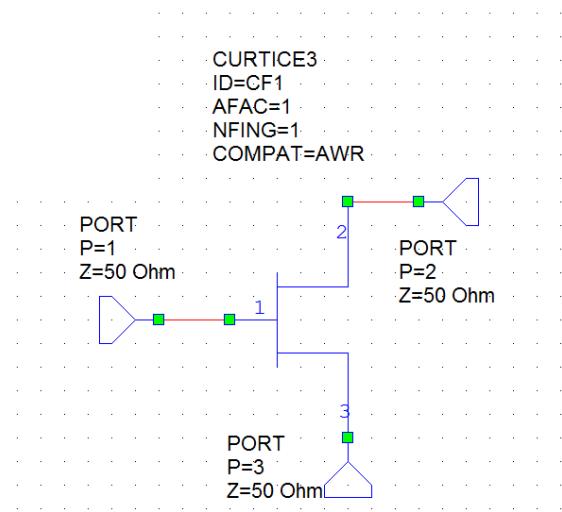
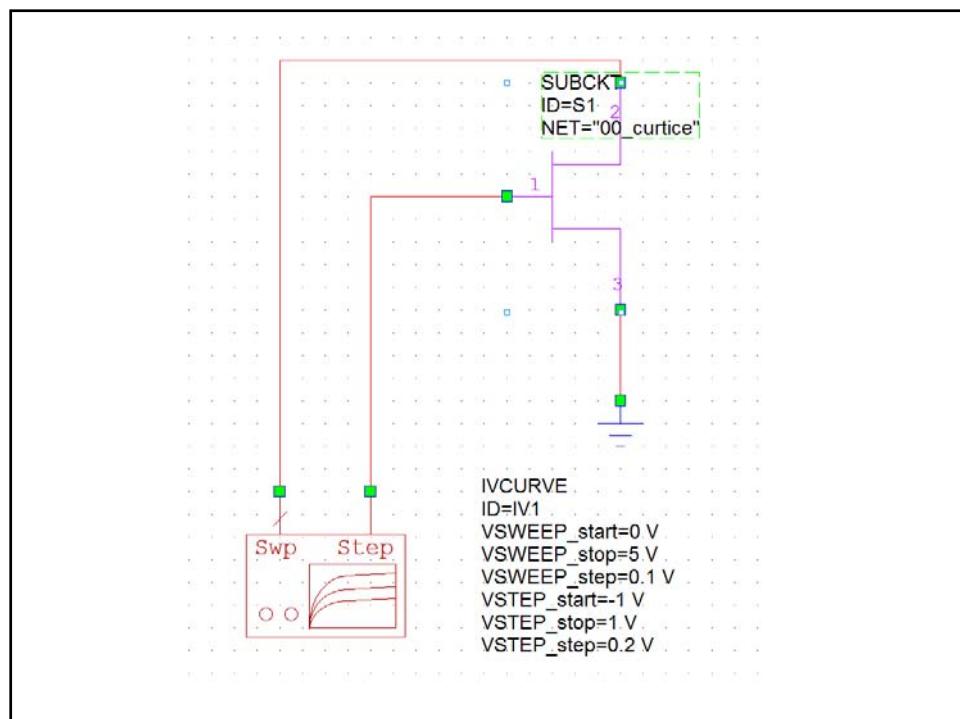
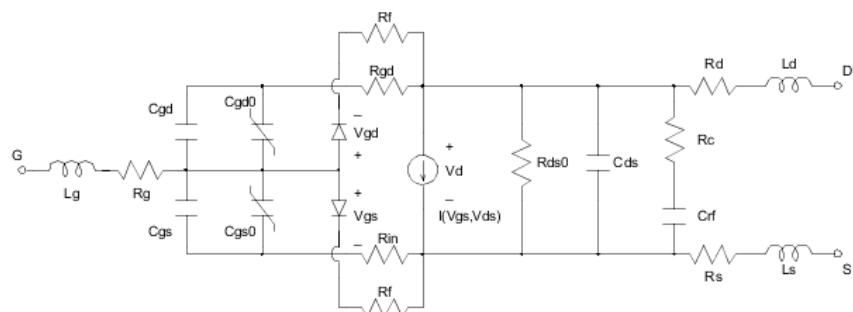


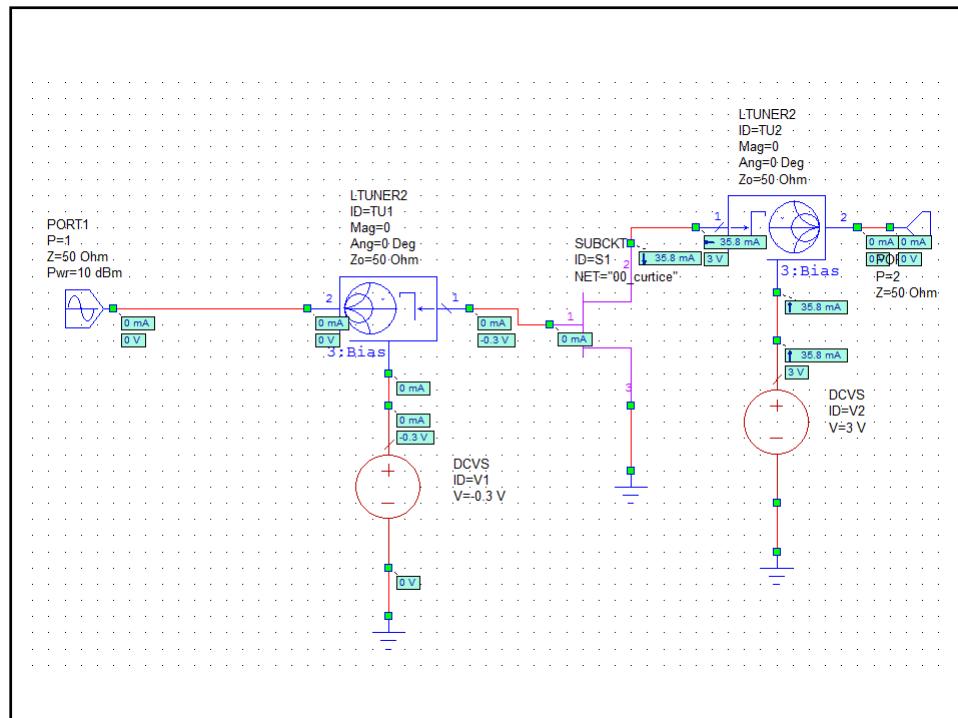
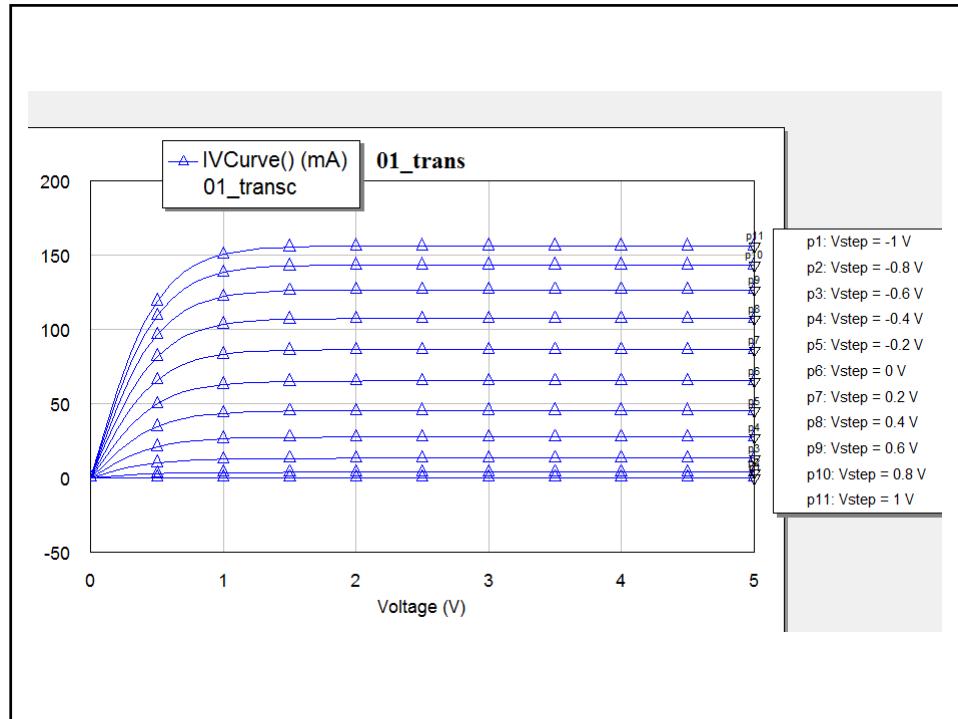
## Progetto di base con MWO di un amplificatore di potenza

Prendere CURTICE3 dalla libreria e porre  
CDS = 0.3 pF, CGS = 0.2 pF, CGD = 0.3 pF

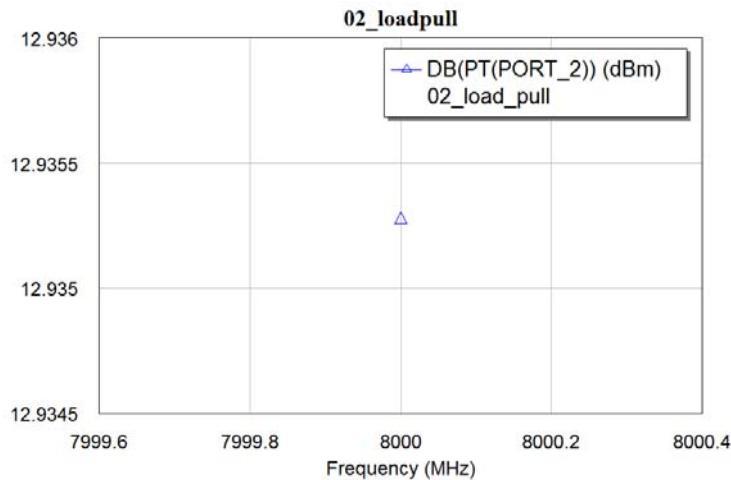


## Modello circuitale CURTICE

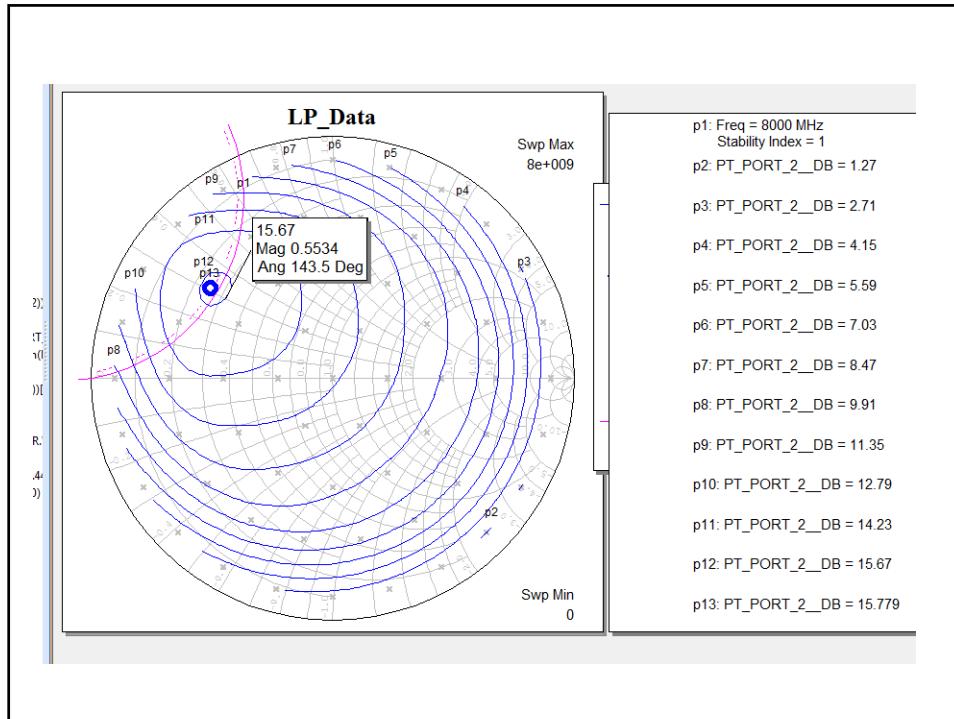




- Scegliere la frequenza di lavoro (8 GHz)
- Aprire un grafico con PT alla porta 2 in dBm



- Lanciare : Scrips -> simulation -> Load\_pull
- Utilizzare valori predefiniti controllando di:
- Scegliere la load pull configuration wizard
- Selezionare LTUNER alla porta 2
- Mettere un marker sul massimo selezionando reflection coefficient mag e ang
- Tracciare la circonferenza di stabilità in uscita



### Circuito per calcolo adattamento in ingresso

